Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.014”**

**ANODE**

**.0095 x .0095”**

**.014”**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .0095” X .0095”**

**Backside Potential: Cathode**

**Mask Ref: CPZ18**

**APPROVED BY: DK DIE SIZE .014” X .014” DATE: 8/31/21**

**MFG: CENTRAL SEMI THICKNESS .0075” P/N: 1N749**

**DG 10.1.2**

#### Rev B, 7/19/02